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| Notice of Allowability | Application No. | Applicant(s) | |
| | 10/808,058 | FORBES, LEONARD | |
| | Examiner Thinh T. Nguyen | Art Unit 2818 | |

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. This communication is responsive to 2/15/2006.
2. The allowed claim(s) is/are 1,4-7.
3. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) All
 - b) Some*
 - c) None
 of the:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) hereto or 2) to Paper No./Mail Date _____.
 - (b) including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. Notice of References Cited (PTO-892)
2. Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date 2/15/06
4. Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. Notice of Informal Patent Application (PTO-152)
6. Interview Summary (PTO-413),
Paper No./Mail Date _____.
7. Examiner's Amendment/Comment
8. Examiner's Statement of Reasons for Allowance
9. Other _____.


 David Nelms
 Supervisory Patent Examiner
 Technology Center 2800

DETAILED ACTION

Examiner's Amendment.

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

As per authorization from the Applicant legal representative; cancels claim 3, and amends independent claim 1 as the following:

-- "A memory transistor comprising:
a substrate having a plurality of source/drain regions, the source/drain regions having a different conductivity type than the remainder of the substrate;
a composite gate insulator layer overlying the substrate;
a metal floating gate overlying the gate insulator wherein an upper portion of the metal floating gate is oxidized, thereby defining a metal oxide inter-gate insulator layer; and
a control gate formed on top of the inter-gate insulator layer

wherein the composite gate insulator layer is comprised of aluminum oxide – aluminum –aluminum oxide wherein the aluminum oxide layer is the oxidized portion of the floating gate. “--

Reason for allowance

2. The following is an examiner's statement of reason for allowance: None of the references of record teaches or suggests the claimed **MEMORY DEVICE WITH HIGH DIELECTRIC CONSTANT GATE DIELECTRICS AND METAL FLOATING GATES SEMICONDUCTOR DEVICE** having the limitations:

-- “a metal floating gate overlying the gate insulator wherein an upper portion of the metal floating gate is oxidized, thereby defining a metal oxide inter-gate insulator layer; and

a control gate formed on top of the inter-gate insulator layer
wherein the composite gate insulator layer is comprised of aluminum oxide – aluminum –aluminum oxide wherein the aluminum oxide layer is the oxidized portion of the floating gate --

And all other limitations as recited in claim 1.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance".

CONCLUSION

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Thinh T Nguyen whose telephone number is 571-272-1790. The examiner can normally be reached on Monday-Friday 9:30am-6:30pm

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David Nelms can be reached at 571-272-1787.

The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval [PAIR] system.

Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available

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through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free)

Thinh T Nguyen 

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David Nelms
Supervisory Patent Examiner
Technology Center 2800